

SYSTEM AND METHOD FOR DISPLAYING A PARALLEL PANEL SIMULTANEOUSLY WITH AN RGB PANEL

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ABSTRACT OF THE DISCLOSURE

A graphics controller includes circuitry for updating multiple display panels over a shared set of data lines associated with the multiple display panels. The circuitry for updating multiple display panels includes circuitry for generating control signals over control lines dedicated to each of the multiple display panels. A memory region
10 configured to store image data for display on the multiple display panels is included with the circuitry for updating multiple display panels. Circuitry configured to select image data associated with one of the multiple display panels for display during an inactive period associated with an other one of the multiple display panels is provided with the circuitry for updating multiple display panels. A method for displaying image data on an
15 RGB panel and a parallel panel simultaneously is also provided.